

L Number	Hits	Search Text	DB	Time stamp
1	30130	memory adj controller	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:02
2	24120	memory with timing with control\$6	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:02
3	1970	(memory with timing with control\$6) with register	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:02
4	1337	access adj timing	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:03
5	78	((memory with timing with control\$6) with register) and (access adj timing)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:03
6	51	(memory adj controller) and (((memory with timing with control\$6) with register) and (access adj timing))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:03
7	18	((memory with timing with control\$6) with register) same (access adj timing)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:06
8	3494	timing same memory same parameters	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:06
11	106	(timing same memory same parameters) same group\$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:07
9	3	(timing same memory same parameters) same mounting	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:07
10	1	(timing same memory same parameters) same grouping	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:08
12	89	(access adj timing) and (timing same memory same parameters)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:08
13	1175	(memory with timing with control\$6) and (timing same memory same parameters)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:08
14	525	(memory adj controller) and (timing same memory same parameters)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:08
15	143	((memory with timing with control\$6) with register) and (timing same memory same parameters)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:09
16	69	memory adj fault adj detect\$6	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:10

17	1	((memory with timing with control\$6) with register) and (access adj timing)) and (memory adj fault adj detect\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:10
18	1	((memory adj controller) and (((memory with timing with control\$6) with register) and (access adj timing))) and (memory adj fault adj detect\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:10
19	1	(timing same memory same parameters) and (memory adj fault adj detect\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:11
20	1	(timing same memory same parameters) and (memory adj fault adj detect\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:11
21	1	(access adj timing) and (memory adj fault adj detect\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:11
22	7	(memory adj controller) and (memory adj fault adj detect\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:12
23	3	(memory with timing with control\$6) and (memory adj fault adj detect\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:13
24	1	((memory with timing with control\$6) with register) and (memory adj fault adj detect\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:14
25	1	(access adj timing) and (memory adj fault adj detect\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:14
26	1139	(711/167).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:16
27	1449	(711/154).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/04/23 17:16

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